

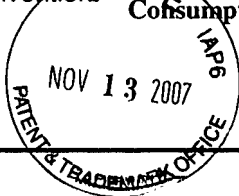
**TRANSMITTAL OF FORMAL DRAWINGS**

Docket No.

ITL.1703US (P17498)

In Re Application Of: **Louis Lippincott, et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/750,075	December 31, 2003	Chikaodili E. Anyikire	47795	2621	2083

Invention: **Method and Apparatus for Estimation Sum of All Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Transmitted herewith are:

six (6) sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).  
Signature

Timothy N. Trop, Reg. No. 28,994  
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Dated: November 8, 2007

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

November 8, 2007

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